



1FW 2822  
[10191/3702]

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor(s) : Maria Del Rocio Martin Lopez et al.  
Serial No. : 10/501,287  
Filing Date : January 18, 2005  
For : SEMICONDUCTOR SYSTEM HAVING A PN  
TRANSITION AND METHOD FOR MANUFACTURING A  
SEMICONDUCTOR SYSTEM  
Group Art Unit : 2822  
Examiner : Tsz K. Chiu  
Confirmation No. : 5683

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

Date: 4/6, 2006

Signature: [Signature]  
Jong H. Lee (Reg. No. 36,197)

**AMENDMENT**

SIR:

In response to the Office Action of January 11, 2006, please amend the above-captioned application without prejudice as follows.

**Amendments to the Claims** are found in the listing of claims, which begins on page 2 of this paper.

**Remarks** begin on page 4 of this paper.